

BUS SYSTEM AND BUS INTERFACE FOR CONNECTION TO A BUS

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Inventor(s):

Applicant(s):

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WO03029996 (A1)

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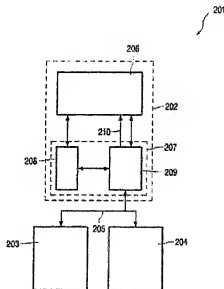
EP1433069 (A1)

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Abstract not available for JP 2005504392 (T)

Abstract of correspondent: **WO 03029996 (A1)**

The invention relates to a bus system comprising a first station (202) and a second station (203, 204) coupled by a bus for transferring data and control signals. The bus is conceived to operate according to a protocol in which the first station (202) repeatedly sends requests to the second station (203, 204). The first station (202) comprises a processor (206), a bus interface (207), and a buffer (208) coupled to said interruptible processor and said bus interface, the processor (206) being operable to generate request properties for the requests to be sent by the first station (202). Furthermore, the processor (206) is operable to handle said data items. The buffer (208) is operable to store said request properties. The bus interface (207) is operable to generate said requests from said stored request properties and to repeatedly send said requests.



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